

2183



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Laurence R. Simar, Jr., et al.

Serial No: 09/702,320

Filed: October 31, 2000

For: Microprocessor With Execution Packet Spanning Two or More Fetch Packets

TI-30559

Art Unit: 2183

Examiner:

Conf. No.: 9784

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**INFORMATION DISCLOSURE STATEMENT**

Ass't Commissioner for Patents  
Washington, DC 20231

Dear Sir:

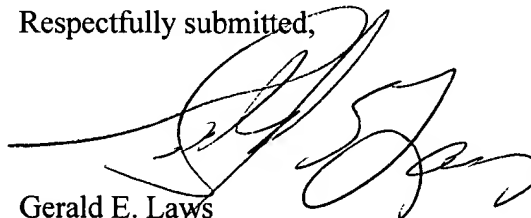
MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)  
I hereby certify that the above correspondence is being deposited with the  
U.S. Postal Service as First Class Mail in an envelope addressed to: Ass't  
Commissioner for Patents, Washington, D.C. 20231 on April 11, 2003.

  
Robin E. Barnum

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. Copies of the noted references are enclosed herewith.

Since this IDS is being submitted before any Office Action, no fee is due.

Respectfully submitted,



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